

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Swoboda et al

Art Unit: 2128

Serial No.: 09/943,599

Confirmation No.: 2479

Filed: August 30, 2001

Examiner: Akash Saxena

Docket: TI-30481

For: CORRELATING ON-CHIP DATA PROCESSOR TRACE INFORMATION FOR  
EXPORT

**Reply Brief under 37 C.F.R. §41.41(a)(1)**

Board of Patent Appeals and Interferences  
United States Patent and Trademark Office  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The EXAMINER'S ANSWER of December 5, 2006 raised no new rejections or new issues. While the Applicants do not agree with the arguments presented in the EXAMINER'S ANSWER, no substantive response is warranted.

If the Examiner has any questions or other correspondence regarding this application, Applicants request that the Examiner contact Applicants' attorney at the below listed telephone number and address to facilitate prosecution.

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Respectfully submitted,  
  
/Robert D. Marshall, Jr./  
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